09/975420 109/975420 109/11/11	ISSUE CLASSIFICATION		PATENT NUMBER
יי, די יידי		JTY Patent Applicati	on
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APPLICATION NO. — 09/975420	CONT/PRIOR-CLASSF	SUBCLASS— ART UNIT	EXAMINER Shute
S Jong-Keun	Ahn		
TITE APPLICANTS TITE APPLICANTS Method and	device for progr	amming nonvolatil	e semiconductor memory PTO-2040 1299

ORIGINAL .			CROSS REFERENCE(S)		
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Figs. Drwg (Assistant Examiner)	G. Print Fig.	Total Claims NOTICE OF ALL	Print Claim for O.G. OWANCE MAILED
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